TO SEPARATE, HOLD TOP AND BOTTOM EDGES, SNAP-APART AND DISCARD CARBON

FORM PTO-892 (REV. 2-92)	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	SERIAL NO.	GROUP ART UNIT	ATTACHMENT TO PAPER NUMBER	
NOTICE OF REFERENCES CITED		10/684,843	2817		3/11/04
		APPLICANT(S) Rokhsaz			

U.S. PATENT DOCUMENTS

*		DOCUMENT NO.	DATE	NAME	CLASS	SUB-CLASS	FILING DATE IF APPROPRIATE
	A	5889432	3-30-99	Ho	330	255	
	B	5903185	5-11-1999	Cargill	330	252	
	C	6201443	3-13-2001	Tanji	330	254	
	D	6552612	4-22-2003	Wilson	330	254	
✓	E	6670847	12-30-2003	Rokhsaz	330	253	
	F						
	G						
	H						
	I						
	J						
	K						

FOREIGN PATENT DOCUMENTS

*		DOCUMENT NO.	DATE	COUNTRY	NAME	CLASS	SUB-CLASS	PERTINENT SHTS. DWG	PERTINENT PP. SPEC.
	L	59-204302	11-19-1984	Japan	Asai	—	—		
	M								
	N								
	O								
	P								
	Q								

OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, Etc.)

R Colinge et al. "Improvement of the performances of SOI CMOS Operational Amplifiers by means of a Gain-Boosting Stage" 1993 IEEE SOI Conference Oct 5-7, 1993 pp 184-185

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EXAMINER	DATE
SHINGLETON	3/11/04

* A copy of this reference is not being furnished with this office action.
(See Manual of Patent Examining Procedure, section 707.05 (a).)